### Letian Ruan

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#### **Education**

### Shanghai Jiao Tong University

2022/08 - 2027/08

Bachelor of Engineering in Mechanical Engineering

Shanghai, China

#### Research

### **System Engineering Research Assistant**

2024/10 - Present

Emerging Parallel Computing Center, advised by Shixuan Sun

SJTU-CSE

- Complete a disaggregated LoRA store server cluster to accelerate inferrring of Large Language Model.
- Build a high-performance computing system for highly-skewed graphs and deploy it on AWS as a serverless service.

## **Open-Source**

**jCourse** The largest unofficial course review platform of SJTU

2024/07 - Present

- As of November 2024, more than 28,000 users have posted over 33,000 reviews for 4,500+ courses.
- One of the main developers and maintainers for the web service based on Next.js and Gin and integrated with Vite, Redis, PostgreSQL and LLM to provide more personalized and faster responses.

# **Projects**

**Chfs** A simple distributed file system similar to Google File System delivered by SJTU IPADS

2024/09 - Present

github: Risc-lt/chfs

- Constructed a basic single-machine inode-based filesystem and extended it into a robust distributed filesystem with file splitting, lock manager and crash recovery.
- Built the MapReduce framework and achieve the Raft consensus algorithm of leader election, heartbeat mechanism, log replication protocol and snapshot mechanism.

 ${\bf BusTub}\ A\ complete\ row-store\ relational\ database\ system$ 

2024/07 - 2024/09

delivered by CMU Database Group

github: Risc-lt/BusTub

- Implemented a buffer pool to improve memory utilization, developed Extensible HashTable for indexing and used Crabbing Protocol to add read-write locks.
- Completed basic SQL operators and optimizers on the volcano model and built a centralized transaction manager based on the MVCC protocol, supporting snapshot isolation.

 $\textbf{xv6-riscv} \ \textit{A basic operating system based on the RISC-V architecture}$ 

2024/02 - 2024/06

delivered by MIT PDOS

github: Risc-lt/xv6-riscv

- Added Lazy Allocation and Copy On Write functionality to the virtual memory module of the xv6 operating system
- Completed thread libraries and synchronization barriers to the kernel module and optimized the buffer pool management in the file system module of xv6 using finer-grained hash tables.

**LSM-KV** A key-value storage engine based on lsm-tree

2024/04 - 2024/05

delivered by SJTU ISCS

github: Risc-lt/LSM-KV

- Built the skiplist and SSTable to form the lsm-tree and used vLog to realize the WiscKey design of key-value separation.
- Implemented basic CURD functions and advanced mechanisms of range queries and garbage collection.

# **Teaching**

#### **UM-SJTU Joint Institute**

Shanghai, China

Teaching Assistant for ENGR1510J Accel. Intro to Comp & Prog

2024/09 - 2024/12

• Worked with the teaching team to coordinate 72 students to reinforce course concepts such as Matlab, c/cpp, data structure & algorithm, and object-oriented programming as well as set up labs and projects for them.

#### **SKills**

- **Programming languages:** C/C++, Golang, Python, Cuda and Typescript.
- Tech Skills: MLSys(Inferring), HPC(Graph), DB(OLTP), OS(Microkernel), Cloud(AWS) and Web(React+Gin/Django)